

**REMARKS**

Claims 2-12 are pending in the above application; by the above amendment, claim 12 has been added.

The Office Action dated June 17, 2005, has been received and carefully reviewed. In that Office Action, it was indicated that claims 2-8 are allowed and that claims 10 and 11 are objected to as being dependent upon a rejected base claim. Claim 9 stands rejected under 35 U.S.C. 102(b) as being anticipated by Onozaki. Because it is believed that claim 9 patentably distinguishes over the art of record, reconsideration and allowance of claim 9 and examination and allowance of new claim 12 is respectfully requested.

Claim 9 requires a data driven type information processing apparatus that includes, *inter alia*, an input/output control circuit outputting a data packet from the information processing apparatus when said data packet includes a host transfer flag. The Office Action indicates that Onozaki includes an input/output control circuit that outputs a data packet from the information processing apparatus when the data packet includes a host transfer flag. However, the portions of Onozaki referred to in the Office Action do not discuss the transfer of a data packet having a certain flag outside the information processing apparatus when a flag is present. Instead, column 14, lines 10-15 describes outputting a packet outside the information processor based on the node number or generation number of the data packet. Onozaki's unfiring flag 107, discussed in paragraph 8 of the Office Action, determines whether a packet is output from a paired data detecting unit. Onozaki, however, in no manner shows or suggests outputting a packet from the information processing apparatus when a host transfer flag is present. Because Onozaki does not show or suggest each element required by claim 9, it is respectfully submitted that claim 9 and its dependent claims 10 and 11 are allowable over the art of record.

New claim 12 is also submitted to be allowable over the art of record. Claim 12 is a method claim that requires that certain actions occur when a dead-lock state occurs. Specifically, when a dead-lock state occurs, the steps of erasing a first one of at least one data packet stored in at least one pipeline register and outputting a second one of at least one data packet from the information processing apparatus to an external host when the data packet includes a host transfer flag are performed. These steps are not shown or suggested by the art of record, and

claim 12 is therefore submitted to be allowable.

CONCLUSION

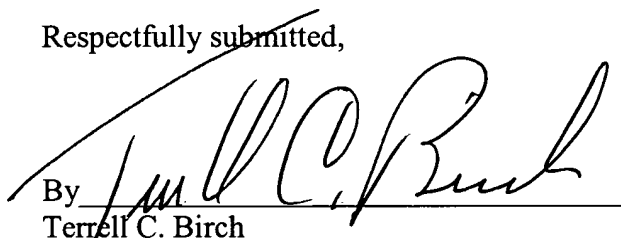
Each issue raised in the Office Action dated June 17, 2005, has been addressed, and it is believed that claims 2-12 are in condition for allowance. Wherefore reconsideration and allowance of claim 9 and examination and allowance of claim 12 is earnestly solicited.

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Scott Wakeman (Reg. No. 37,750) at the telephone number of the undersigned below, to conduct an interview in an effort to expedite prosecution in connection with the present application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

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Respectfully submitted,



By  
Terrell C. Birch  
Registration No.: 19,382  
Scott T. Wakeman  
Registration No.: 37,750  
BIRCH, STEWART, KOLASCH & BIRCH, LLP  
8110 Gatehouse Rd  
Suite 100 East  
P.O. Box 747  
Falls Church, Virginia 22040-0747  
(703) 205-8000  
Attorneys for Applicant